

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently Amended) An apparatus for inspecting errors in a system board including a number of channels for controlling the overall operation of a system, comprising:

judging means for judging signals respectively received in the number of channels of the system board;

comparing means for comparing the signals respectively received in the number of channels of the system board ~~to output signals respectively informing if the channels are failed or not through digital operation and finding a failed channel to output information of the failed channel~~; and

~~means for respectively transmitting normal signals to the failed channels, which are respectively inputted by said comparing means, to maintain the operation of the system board~~

signal-maintaining means for performing digital operation of judging signals of the failed channels from the comparing means and carrying out feedback to improve stability of system trip,

wherein the judging means is MISO type generating a normal command signal removing errors after judging errors by voting the signals received from a number of channels of the system board through digital operation.

2. (Currently Amended) An apparatus for inspecting errors in a system board according to claim 1, wherein said judging means, comparing means and maintaining means are provided ~~as the form of~~ in a single chip.

3. (Original) An apparatus for inspecting errors in a system board according to claim 1, wherein said judging means comprises:

a bypass terminal for separating a signal of the failed channel when interruption of the signal is required due to repair of the failed channel in the system board; and

a manual trip terminal capable of forcibly changing the output value of said judging means to convert the operation.

4. (Original) An apparatus for inspecting errors in a system board according to claim 1, wherein said comparing means compares the input signals in the channels of the system board, and if a channel outputs a signal different from the other channels, outputs a signal informing that the channel outputting the different signal is failed.

5. (Original) An apparatus for inspecting errors in a system board according to claim 1, wherein said maintaining means comprises a logic circuit for transmitting an operation signal simultaneously with the signal outputted from the failed channel in

the system board to normally maintain the operation of the system board upon receiving the failed channel informing signals.

6. (Original) An apparatus for inspecting errors in a system board according to claim 5, wherein said logic circuit is an OR gate.

7. (Canceled)